CLAIM AMENDMENTS:

Please amend the claims as follows:

1. (Currently amended) A clock generator module, electrically connected with a mainboard through a connector for communicating with each other, wherein

an interface definition of the clock generator communicates with a control logic circuit of the mainboard to generate a required clock frequency; wherein

said interface definition comprises identification signals, interface control signals; a reference clock input signal, a clock output signal and power supply; and wherein

different clock generator modules use different digital levels of said identification signals to differentiate functions;

said interface control signals are for setting up various clock modules;

said reference clock input signal selectively receives a clock reference input from a selected reference clock source;

said clock output signal outputs the required clock signal to said mainboard, and said mainboard provides power supply to said clock generator module.

2. (Cancelled)

3. (Currently amended) The clock generator module according to claim [[2]] 1, wherein said identification signals ~~allow~~ identify a required clock type to said clock generator module ~~to identify the required clock type~~.

4. (Original) The clock generator module according to claim 3, wherein said identification signals function are in a three-bit identification mode.

5. (Currently amended) The clock generator module according to claim [[2]] 1, wherein said interface control signals use ten signals for setting up various clock generator modules.

6. (Currently amended) The clock generator module according to claim [[2]] 1, wherein said interface control signals are generated by said control logic circuit according to retrieved identification signals.

7. (Original) The clock generator module according to claim 1, wherein said mainboard is integrated into stand-alone equipment.

8. (Original) The clock generator module according to claim 1, wherein said mainboard is integrated in a modular platform or equipment.

9. (Original) The clock generator module according to claim 1, wherein said mainboard is integrated into a data-acquisition electronic product.

10. (Original) The clock generator module according to claim 1, wherein said mainboard is integrated into an electronic product comprising data-acquisition function.